

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3147	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:49
L2	3702	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:49
L3	1132	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:50
L4	3147	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L5	3702	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L6	1132	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L7	48762	L4 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L8	1169	(L5 L6) and L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52

EAST Search History

L9	932887	(module or pcb or (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L10	256760	L9 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L11	325	L8 and L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L12	151	(refresh\$3 near3 control\$4) and L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L13	10122	(refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L14	151	L13 and L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L15	9971	L13 not L14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L16	151	L13 not L15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52

EAST Search History

L17	854	(refresh\$3 near3 control\$4) with (chip IC (integrated adj3 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L18	19	L16 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L19	4898	(361/728 361/736 361/748 361/760 361/761).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L20	57	L19 and (DIMM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L21	3147	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L22	3702	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L23	1132	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L24	48762	L21 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52

EAST Search History

L25	1169	(L22 L23) and L24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L26	932887	(module or pcb or (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L27	256760	L26 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L28	325	L25 and L27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L29	151	(refresh\$3 near3 control\$4) and L28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L30	0	L20 and L29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:52
L31	0	L20 and (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:53
L32	21	L20 and (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:53

EAST Search History

L33	4	L20 and (controller) and refresh\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:53
L34	1	(memory and (module DIMM) and (substrate bulk) and (refresh\$3 near3 control\$4) and (input near5 address) and (input near5 command) and counter near5 (row column) and tim\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/03 13:57
S35	3	(refresh\$3 near3 controller) same (mount\$3 locat\$3) same (memory near3 module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:04
S36	1	(refresh\$3 near3 controller) with (mount\$3 locat\$3) with (memory near3 module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 14:59
S37	1	(refresh\$3 near3 controller) with (mount\$3 locat\$3) with ((memory near3 module) PCB (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:05